

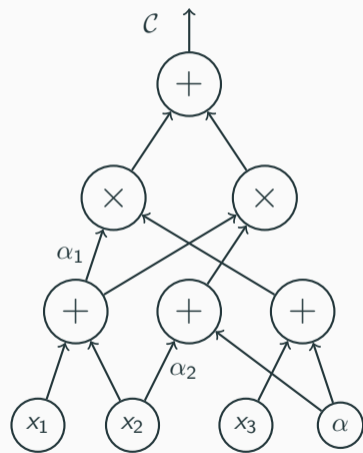
New Lower Bounds against Homogeneous Non-Commutative Circuits

Prerona Chatterjee [joint work with Pavel Hrubeš (Institute of Mathematics, CAS)]

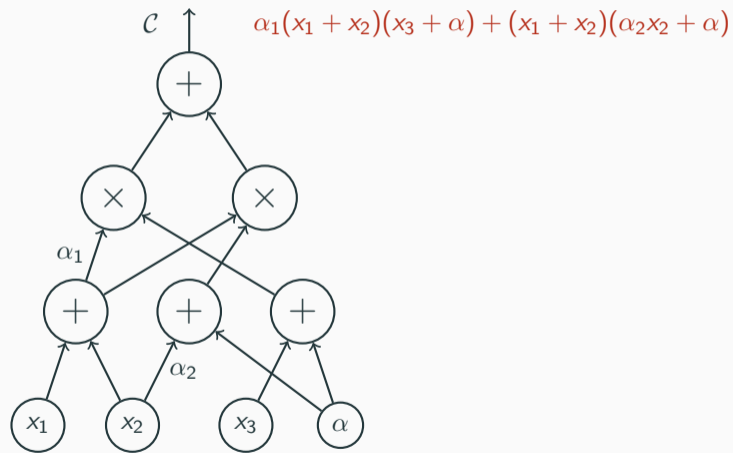
Tel Aviv University

July, 19, 2023

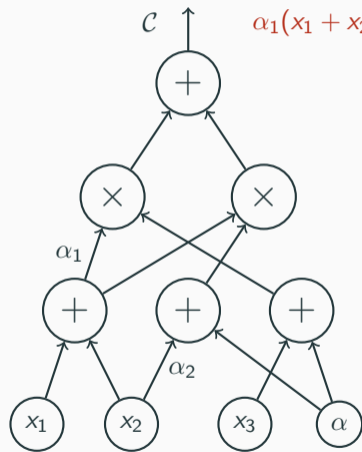
Algebraic Circuits



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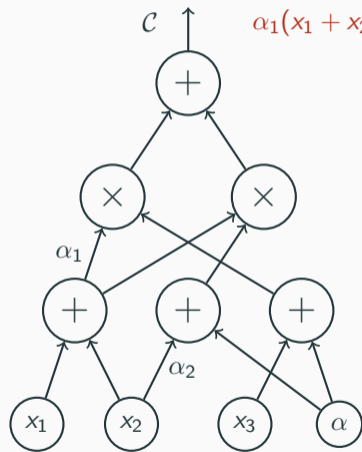


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Objects of Study

Polynomials over n variables of degree d .

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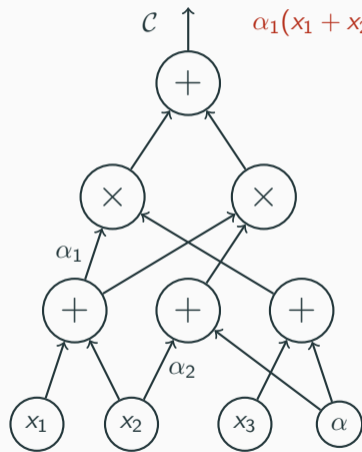
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Central Question: Find **explicit** polynomials that cannot be computed by **efficient** circuits.

[Baur-Strassen]: Any algebraic circuit computing $\sum_{i=1}^n x_i^d$ requires $\Omega(n \log d)$ wires.

The Non-Commutative Setting

$$f(x, y) = (x + y) \times (x + y) = x^2 + xy + yx + y^2 \neq x^2 + 2xy + y^2$$

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[Carmossino-Impagliazzo-Lovett-Mihajlin]

$$\Omega(n^{\frac{\omega}{2} + \epsilon}) \text{ for } f_{n,c} \implies \Omega(2^n) \text{ for } f'_{n,n}$$

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Theorem: Any homogeneous non-commutative circuit computing

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has size $\Omega(nd)$ for $d \leq \frac{n}{2}$.

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Further, there is a non-commutative circuit of size $O(n \log^2 n)$ that computes $\text{OSym}_{n,n/2}(\mathbf{x})$.

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Main Observation: For any f that is computable by a homogeneous non-commutative circuit of size s ,

$$\mu(f) \leq s + 1.$$

A simple proof of an obvious fact

\mathcal{C} : Homogeneous non-commutative circuit.

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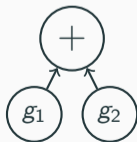
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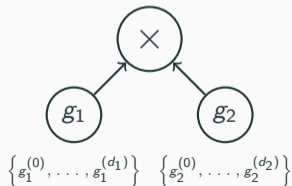
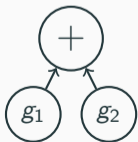
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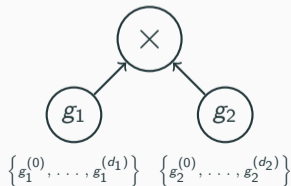
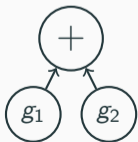
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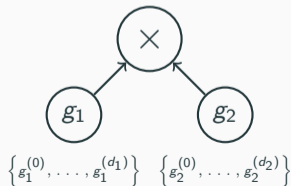
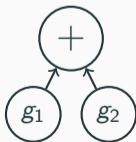
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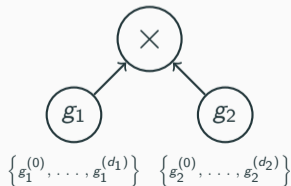
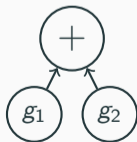
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Therefore, $\mu(\mathcal{C}_f) \geq n$.

Using it to prove a “not so obvious” fact

Theorem: There exists an explicit monomial over $\{x_0, x_1\}$ of degree d such that any homogeneous non-commutative circuit computing it must have size $\Omega\left(\frac{d}{\log d}\right)$.

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In this case, if \mathcal{C} is a homogeneous non-commutative circuit of size s , then $\mu_\ell(\mathcal{C}) \leq O(s \log d)$.

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Therefore all we need is a monomial, f , over $\{x_0, x_1\}$ of degree d such that $\mu_\ell(f) \geq \Omega(d)$.

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de Bruijn Sequence (of order $\log d$): It is a cyclic sequence in the alphabet $\{0, 1\}$ in which every string of length $\log d$, occurs exactly once as a substring.

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Question: Can we prove the same lower bound against general non-commutative circuits?

Getting back to the main result

[Baur-Strassen]: If there is a circuit of size s computing $f \in \mathbb{F}[\mathbf{x}]$, then there is a circuit of size at most $5s$ that simultaneously compute $\{\partial_{x_1} f, \partial_{x_2} f, \dots, \partial_{x_n} f\}$.

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Note: $f = x_1 B_d(x_0^{(1)}, x_1^{(1)}) + \dots + x_n B_d(x_0^{(n)}, x_1^{(n)})$ already (almost) has the required property.

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Therefore we have an $\Omega(nd)$ lower bound against homogeneous non-commutative circuits.

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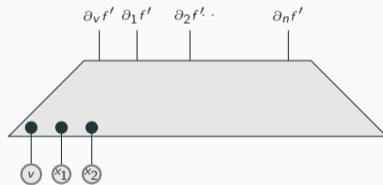
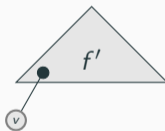
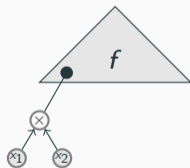
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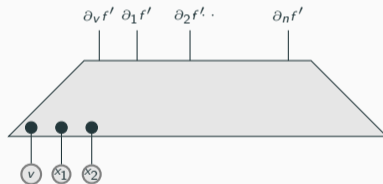
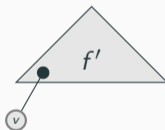
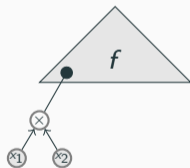
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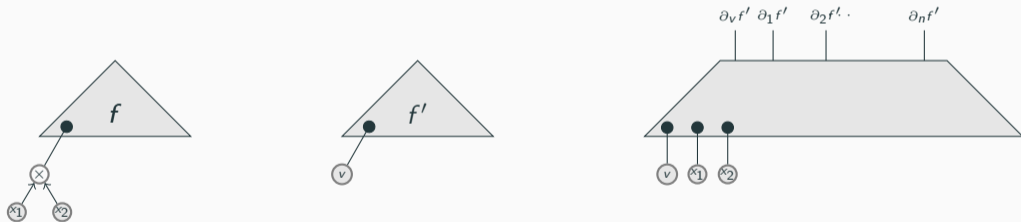


Step 2: Write each of $\{\partial_i f\}_i$ using $\partial_v f'$ and $\{\partial_i f'\}_i$.

Proof of [Baur-Strassen]

[Baur-Strassen]: If there is a circuit of size s computing $f \in \mathbb{F}[\mathbf{x}]$, then there is a circuit of size at most $5s$ that simultaneously compute $\{\partial_{x_1} f, \partial_{x_2} f, \dots, \partial_{x_n} f\}$.

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Step 2: Write each of $\{\partial_i f\}_i$ using $\partial_v f'$ and $\{\partial_i f'\}_i$. Add (the ≤ 10 extra) edges accordingly.

Making [Baur-Strassen] work in the homogeneous setting

Target: If there is a homogeneous circuit of size s computing $f \in \mathbb{F}[\mathbf{x}]$, then there is a homogeneous circuit of size at most $5s$ that simultaneously compute $\{\partial_{x_1} f, \partial_{x_2} f, \dots, \partial_{x_n} f\}$.

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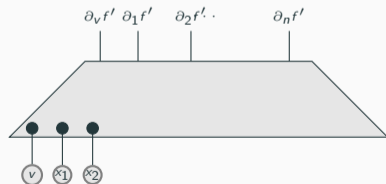
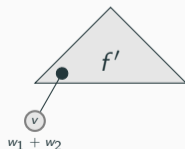
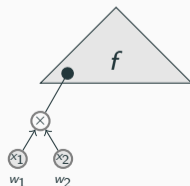
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Making [Baur-Strassen] work in the non-commutative setting

Formal derivatives (with respect to the first position)

Given a polynomial f and a variable x , f can be uniquely written as

$$f = x \cdot f_0 + f_1$$

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Chain rules can be defined formally as well.

Lemma: If there is a homogeneous NC circuit of size s computing $f \in \mathbb{F}[\mathbf{x}]$, then there is a homogeneous NC circuit of size at most $5s$ that simultaneously compute $\{\partial_{1,x_1}f, \dots, \partial_{1,x_n}f\}$.

$$f = \text{OSym}_{n,d}(\mathbf{x}) = \sum_{1 \leq i_1 < \dots < i_d \leq n} x_{i_1} \cdots x_{i_d}$$

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- There is a non-commutative circuit of size $O(n \log^2 n)$ that computes all the elementary symmetric polynomials simultaneously.
 - This shows a **super-linear separation** between homogenous and non-homogeneous non-commutative circuits.

[CILM] and how our result fits in

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$$s = Nd \implies N^\varepsilon = d \implies s' = d < \frac{cn}{\varepsilon}d = nD$$

Open Questions

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If true, then the answer to the second question is "yes".

Thank you!