

# Lower Bounds Against Non-Commutative Models of Algebraic Computation

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**Prerona Chatterjee** (joint work with Pavel Hrubeš)

IIT Madras

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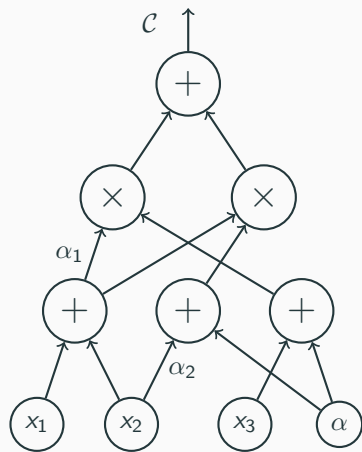
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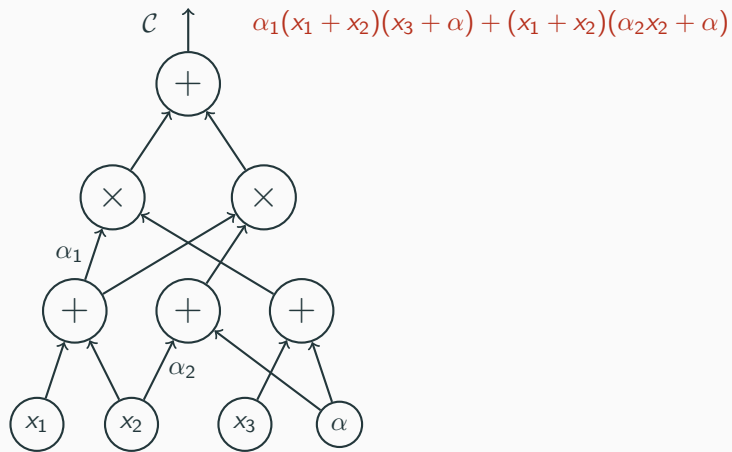
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**Model of interest today:** Algebraic Circuits

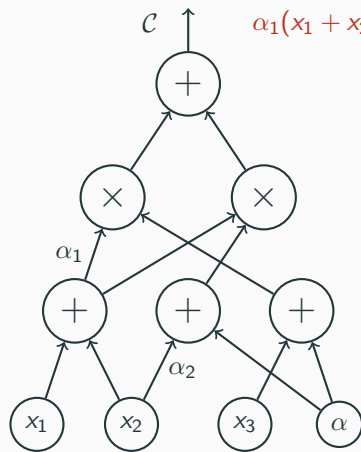
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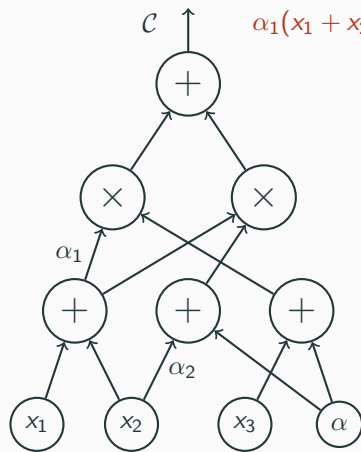


## Objects of Study

Polynomials over  $n$  variables of degree  $d$ .



# Algebraic Circuits



$$\alpha_1(x_1 + x_2)(x_3 + \alpha) + (x_1 + x_2)(\alpha_2 x_2 + \alpha)$$

## Objects of Study

Polynomials over  $n$  variables of degree  $d$ .

**Central Question:** Find **explicit** polynomials that cannot be computed by circuits of size  **$\text{poly}(n,d)$** .

# What is known?

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## **Super-polynomial Lower Bound Against Constant Depth Circuits**

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**This is especially cool in the algebraic world.**

Depth reduction results exist, which show that "good enough" super-polynomial lower bounds against constant depth circuits imply super-polynomial lower bounds against general circuits.

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**Find an explicit polynomial that is hard!**

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**Can we do something better in this setting?**

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**Theorem:** Any homogeneous non-commutative circuit computing

$$\text{OSym}_{n,d} = \sum_{1 \leq i_1 < \dots < i_d \leq n} x_{i_1} \cdots x_{i_d}$$

has size  $\Omega(nd')$  where  $d' = \min(d, n - d)$ .

## A simple proof of an obvious fact

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**Example:**  $f = x_1 \cdots x_d \implies f^{(0)} = x_1, f^{(d)} = x_d, f^{(i)} = x_i x_{i+1}$  for every  $1 \leq i \leq d - 1$ .

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$\mathcal{C}$ : Homogeneous non-commutative circuit.

$$\mu(\mathcal{C}) = \text{rank} \left( \text{span}_{\mathbb{F}} \left( \bigcup_{g \in \mathcal{C}} \left\{ g^{(0)}, g^{(1)}, \dots, g^{(d)} \right\} \right) \right).$$

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## Using it to prove a “not so obvious” fact

**Theorem:** There exists an explicit monomial over  $\{x_0, x_1\}$  of degree  $d$  such that any homogeneous non-commutative circuit computing it must have size  $\Omega\left(\frac{d}{\log d}\right)$ .

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**The tweak:** For a homogeneous non-commutative polynomial  $f$  of degree  $d$ , define

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In this case, if  $\mathcal{C}$  is a homogeneous non-commutative circuit of size  $s$ , then  $\mu_\ell(\mathcal{C}) \leq O(s \log d)$ .

Therefore all we need is a monomial,  $f$ , over  $\{x_0, x_1\}$  of degree  $d$  such that  $\mu_\ell(f) \geq \Omega(d)$ .

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**Question:** Can we prove the same lower bound against general non-commutative circuits?

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**[Baur-Strassen]:** If there is a circuit of size  $s$  computing  $f \in \mathbb{F}[\mathbf{x}]$ , then there is a circuit of size at most  $5s$  that simultaneously compute  $\{\partial_{x_1} f, \partial_{x_2} f, \dots, \partial_{x_n} f\}$ .

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Note:  $f = x_1 B_d(x_0^{(1)}, x_1^{(1)}) + \dots + x_n B_d(x_0^{(n)}, x_1^{(n)})$  already (almost) has the required property.

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Therefore we have an  $\Omega(nd)$  lower bound against homogeneous non-commutative circuits.

Note:  $f$  has a non-homogeneous non-commutative circuit of size  $O(n \log^2 d)$ .

## Proof of [Baur-Strassen]

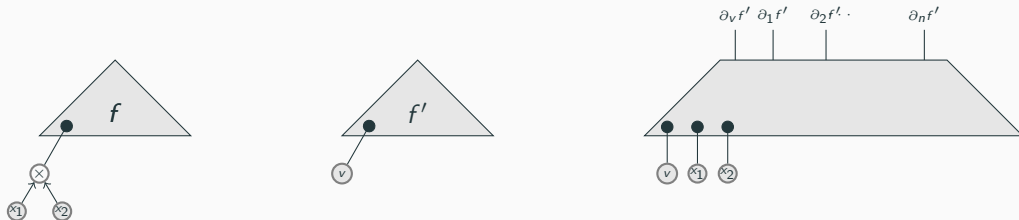
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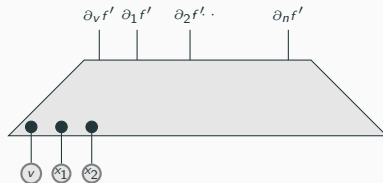
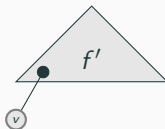
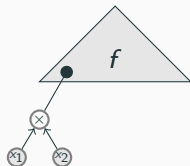
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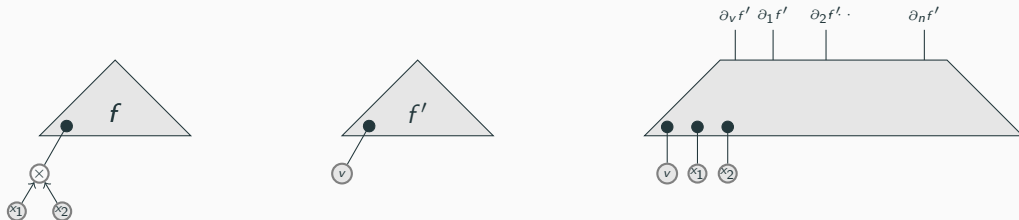


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**Step 2:** Write each of  $\{\partial_i f\}_i$  using  $\partial_v f'$  and  $\{\partial_i f'\}_i$ . Add (the  $\leq 10$  extra) edges accordingly.

## Making [Baur-Strassen] work in the homogeneous setting

**Target:** If there is a homogeneous circuit of size  $s$  computing  $f \in \mathbb{F}[\mathbf{x}]$ , then there is a homogeneous circuit of size at most  $5s$  that simultaneously compute  $\{\partial_{x_1} f, \partial_{x_2} f, \dots, \partial_{x_n} f\}$ .

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**Lemma:** If there is a  $\mathbf{w}$ -homogeneous circuit of size  $s$  computing  $f \in \mathbb{F}[\mathbf{x}]$ , then there is a  $\mathbf{w}$ -homogeneous circuit of size at most  $5s$  that simultaneously compute  $\{\partial_{x_1} f, \partial_{x_2} f, \dots, \partial_{x_n} f\}$ .

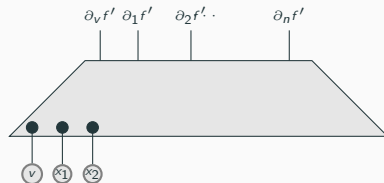
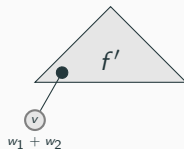
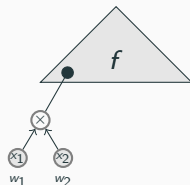
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# Making [Baur-Strassen] work in the non-commutative setting

## Formal derivatives (with respect to the first position)

Given a polynomial  $f$  and a variable  $x$ ,  $f$  can be uniquely written as

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**Chain rules can be defined formally as well.**

**Lemma:** If there is a homogeneous NC circuit of size  $s$  computing  $f \in \mathbb{F}[\mathbf{x}]$ , then there is a homogeneous NC circuit of size at most  $5s$  that simultaneously compute  $\{\partial_{1,x_1}f, \dots, \partial_{1,x_n}f\}$ .

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Use the fact that  $\mu(\text{out}(\mathcal{C}')) \leq \mu(\mathcal{C}')$  to complete the proof.

## Recalling the measure and the polynomial

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$$\mu(f_1, \dots, f_n) = \text{rank} \left( \text{span}_{\mathbb{F}} \left( \bigcup_{i=1}^n \{f_i^{(0)}, f_i^{(1)}, \dots, f_i^{(d)}\} \right) \right).$$

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### The hard polynomial

$$\text{OSym}_{n, \frac{n}{2}+1}(\mathbf{x}) = \sum_{1 \leq i_1 < \dots < i_{\frac{n}{2}+1} \leq n} x_{i_1} x_{i_2} \cdots x_{i_{\frac{n}{2}+1}}$$

## Polynomial with a large measure

$$f = \text{OSym}_{n, \frac{n}{2}+1}(\mathbf{x}) = \sum_{1 \leq i_1 < \dots < i_{\frac{n}{2}+1} \leq n} x_{i_1} x_{i_2} \cdots x_{i_{\frac{n}{2}+1}}$$

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**Claim:** The following set of size  $\Omega(n^2)$  is linearly independent.

$$\left\{ f_i^{(j)} : 1 \leq i \leq \frac{n}{2}, \quad 0 < j < \frac{n}{2} \right\}.$$



# Polynomial with a large measure

$$x_{\frac{n}{2}+1}x_{\frac{n}{2}+2} \cdots x_2x_{\frac{n}{2}+2} \cdots \cdots x_{n-2}x_{n-1} \cdots x_{\frac{n}{2}-1}x_{n-1} \quad x_{n-1}x_n \cdots x_{\frac{n}{2}}x_n$$

$$(1, \frac{n}{2})$$

$$\vdots$$

$$(1, 1)$$

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$$x_k x_l$$

$$(j, i)$$

$$\boxed{\text{coeff}_{x_k x_l}(f_i^{(j)})}$$

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$(j, i)$	$\text{coeff}_{x_k x_l}(f_i^{(j)})$
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The matrix is lower triangular with the diagonal entries being all 1.

This completes the proof of the main result.

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Use the following fact recursively.

$$\text{OSym}_{n,d}(x_1, \dots, x_n) = \text{OSym}_{n-1,d-1}(x_1, \dots, x_{n-1}) \cdot x_n + \text{OSym}_{n-1,d}(x_1, \dots, x_{n-1}).$$

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Think of  $f = \prod_{i=1}^{\frac{n}{2}} (1 + tx_i), g = \prod_{i=\frac{n}{2}+1}^n (1 + tx_i) \in \mathbb{F} \langle \mathbf{x} \rangle [t]$ .

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Do polynomial multiplication recursively  $\log n$  times. Note that polynomial multiplication can be done in time  $O(n \log n)$  using FFT.

# Open Questions

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**Conjecture:** If

$$f = x_1 x_0^{d-1} f_1 + x_0 x_1 x_0^{d-2} f_2 + \cdots + x_0^{d-1} x_1 f_d$$

can be computed by a non-commutative circuit of size  $s$ , then  $\{f_1, \dots, f_d\}$  can be simultaneously computed by a non-commutative circuit of size  $d + O(s)$ .

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If true, then the answer to the second question is "yes".

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# Hardness Amplification

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**Question:** Can we show a similar statement (or any non-trivial hardness amplification statement) in the non-constant degree setting?

**Thank you!**